FIG. 1

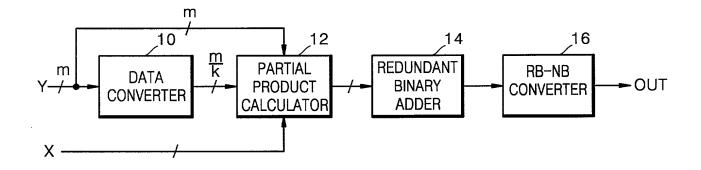
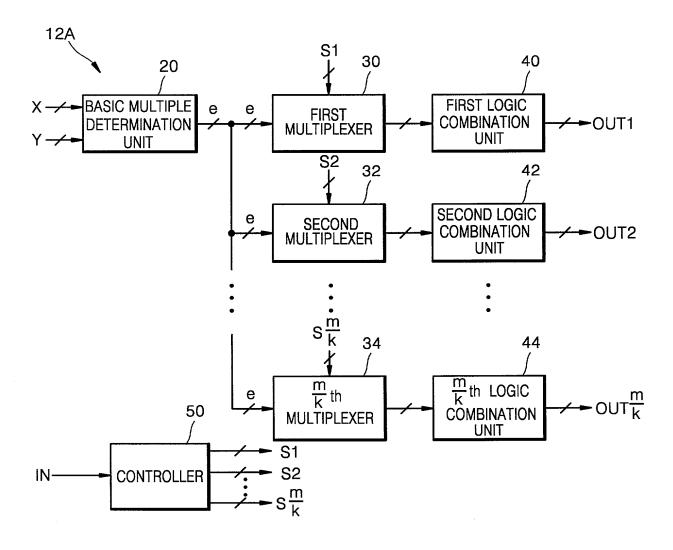


FIG. 2



Title: Apparatus And Method For Digital Multiplication Using Redundant Binary Arithmetic

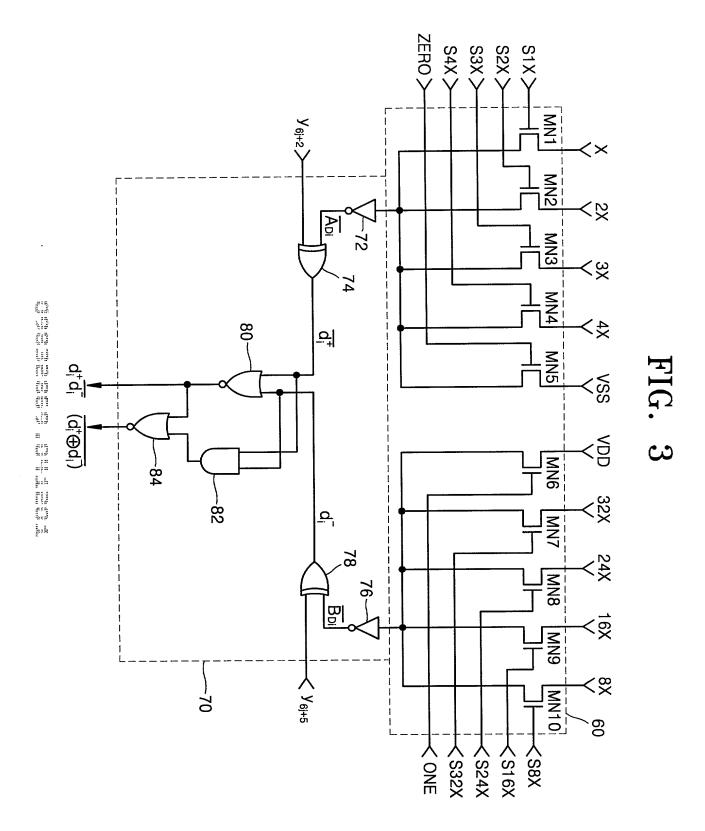


FIG. 4

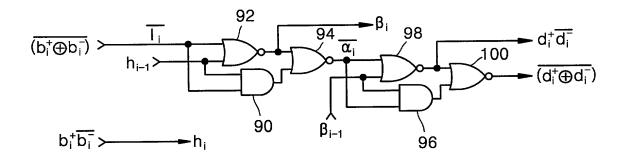
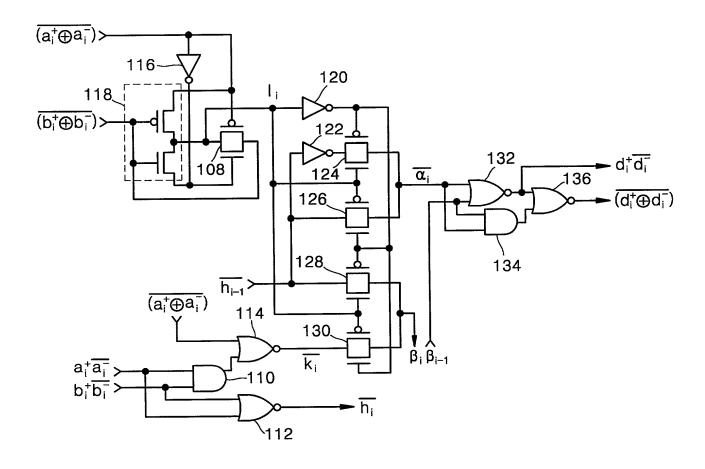


FIG. 5



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FIG. 6

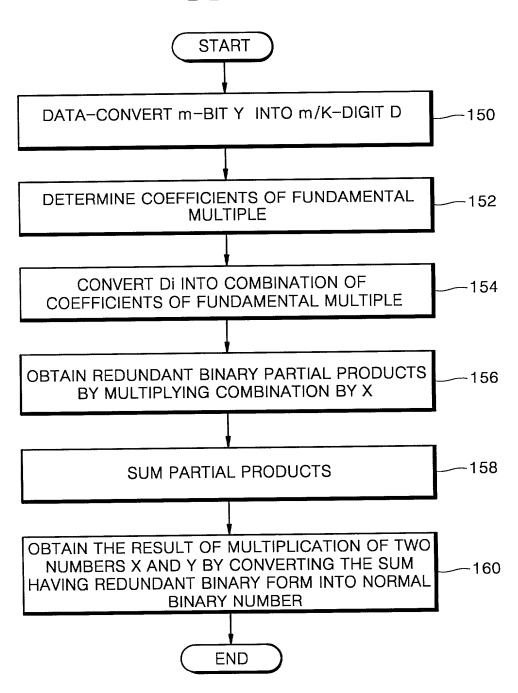


FIG. 7

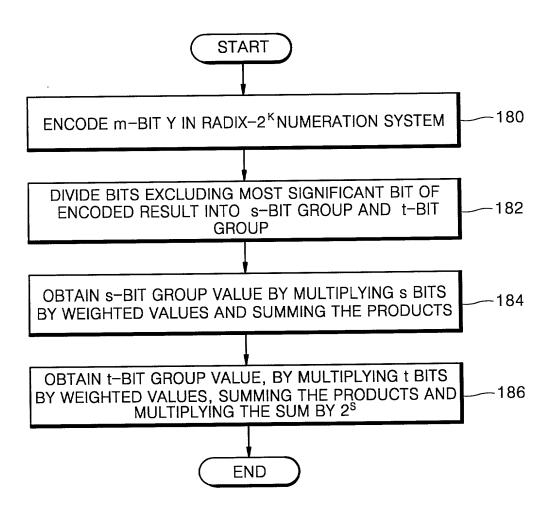


FIG. 8

